

Technical Bulletin #13**To:** Coulometrics Support Personnel**From:** Engineering Dept.**Date:** August 15, 1995**Subject:** Dip switches for Purge time and Drop time CM110-046 PCB on CM5200**Purpose:**

The purpose of this memo is to supply UIC personnel with switch settings for Purge and Drop time on the CM110-046 Microcontroller board of the CM5200 Autosampler.

Information:

The following chart shows the switch settings for the delay times for Purge and drop times. Attached to this sheet is a pictorial of the board showing the location of the Purge switch (S2), and the DROP switch (S4). These are also marked on the silk screen of the CM110-046 Circuit board. All times are in seconds.

| | SW1 | SW2 | SW3 | SW4 | PURGE TIME | DROP TIME |
|----|--------|--------|--------|--------|---------------|--------------|
| 15 | OPEN | OPEN | OPEN | OPEN | 17.2 | 4.3 |
| 14 | CLOSED | OPEN | OPEN | OPEN | 16.0 | 4.1 |
| 13 | OPEN | CLOSED | OPEN | OPEN | 14.8 | 3.6 |
| 12 | CLOSED | CLOSED | OPEN | OPEN | 13.7 | 3.4 |
| 11 | OPEN | OPEN | CLOSED | OPEN | 12.4 | 3.2 |
| 10 | CLOSED | OPEN | CLOSED | OPEN | 11.3 | 2.8 |
| 9 | OPEN | CLOSED | CLOSED | OPEN | 10.5 | 2.7 |
| 8 | CLOSED | CLOSED | CLOSED | OPEN | 9.9 | 2.5 |
| 7 | OPEN | OPEN | OPEN | CLOSED | 9.2 | 2.0 |
| 6 | CLOSED | OPEN | OPEN | CLOSED | 7.0 | 1.8 |
| 5 | OPEN | CLOSED | OPEN | CLOSED | 6.5 | 1.5 |
| 4 | CLOSED | CLOSED | OPEN | CLOSED | 4.6 | 1.2 |
| 3 | OPEN | OPEN | CLOSED | CLOSED | 3.6 | .8 |
| 2 | CLOSED | OPEN | CLOSED | CLOSED | 3.5 | .6 |
| 1 | OPEN | CLOSED | CLOSED | CLOSED | .9 | .5 |
| 0 | CLOSED | CLOSED | CLOSED | CLOSED | 73.0 | 73.0 |

CM110-046 8031 MCU PCB



